

Amendments to the Claims

1. (CURRENTLY AMENDED) A data carrier ~~(1)~~ that is arranged to receive a signal ~~(S)~~ in a non-contacting manner and that has an electrical circuit ~~(2)~~, to which circuit ~~(2)~~ the signal ~~(S)~~ can be fed and which circuit ~~(2)~~ is arranged, by using the signal ~~(S)~~, to generate a supply voltage ~~(V)~~ for parts of the circuit, which circuit ~~(2)~~ comprises storage means ~~(5)~~ that are arranged to store information capacitively, the information being represented by a value of an information voltage ~~(UI)~~ arising at the storage means ~~(5)~~, and which circuit ~~(2)~~ comprises information-voltage generating means ~~(6)~~ that are arranged to receive a control signal ~~(CS)~~, which control signal ~~(CS)~~ is of a voltage value that is at most equal to the value of the supply voltage ~~(V)~~, and that are arranged to generate the information voltage ~~(UI)~~ by using the control signal ~~(CS)~~, characterized in that the information-voltage generating means ~~(6)~~ have voltage-raising means ~~(8)~~ that are arranged to raise the voltage value of the control signal ~~(CS)~~.
2. (CURRENTLY AMENDED) A data carrier ~~(1)~~ as claimed in claim 1, characterized in that the voltage-raising means ~~(8)~~ are implemented in the form of a charge pump ~~(10)~~ that is arranged to raise the voltage value of the control signal ~~(CS)~~ by the value of the supply voltage ~~(V)~~.
3. (CURRENTLY AMENDED) A data carrier 1 as claimed in claim 1, characterized in that the information-voltage generating means ~~(6)~~ have voltage-limiting means ~~(9)~~ that are arranged to limit the raising of the voltage value of the control signal ~~(CS)~~.
4. (CURRENTLY AMENDED) A circuit ~~(2)~~ for a data carrier ~~(1)~~, which data carrier ~~(1)~~ is arranged to receive a signal ~~(S)~~ in a non-contacting manner, to which circuit ~~(2)~~ the signal ~~(S)~~ can be fed and which circuit ~~(2)~~ is arranged, by using the signal ~~(S)~~, to generate a supply voltage ~~(V)~~ for parts of the circuit ~~(2)~~, which circuit ~~(2)~~ comprises storage means ~~(5)~~ that are arranged to store information capacitively, the information being represented by a value of an information voltage UI arising at the storage means ~~(5)~~, and which circuit ~~(2)~~ comprises information-

voltage generating means ~~(6)~~ that are arranged to receive a control signal ~~(CS)~~, which control signal ~~(CS)~~ is of a voltage value that is at most equal to the value of the supply voltage ~~(V)~~, and that are arranged to generate the information voltage ~~(UI)~~ by using the control signal ~~(CS)~~, characterized in that the information-voltage generating means ~~(6)~~ have voltage-raising means ~~(8)~~ that are arranged to raise the value of the voltage of the control signal ~~(CS)~~.

5. (CURRENTLY AMENDED) A circuit ~~(2)~~ as claimed in claim 4, characterized in that the voltage-raising means ~~(8)~~ are implemented in the form of a charge pump ~~(10)~~ that is arranged to raise the voltage value of the control signal ~~(CS)~~ by the value of the supply voltage ~~(V)~~.

6. (CURRENTLY AMENDED) A circuit ~~(2)~~ as claimed in claim 4, characterized in that the information-voltage generating means ~~(6)~~ have voltage-limiting means ~~(9)~~ that are arranged to limit the raising of the voltage value of the control signal ~~(CS)~~.

7. (CURRENTLY AMENDED) A circuit ~~(2)~~ as claimed in claim 4, characterized in that the circuit is implemented in the form of an integrated circuit.